

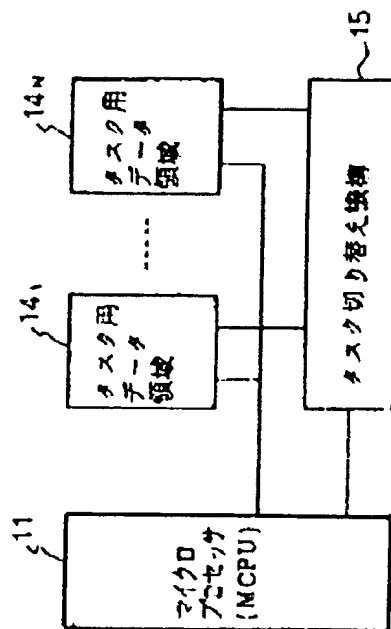
**TASK SWITCHING SYSTEM**

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**Classification:**  
- international: G06F9/46  
- european:  
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**Abstract of JP2217929**

**PURPOSE:** To completely prevent the generation of interference between tasks and to improve reliability by allocating a specific address space area to a data area in each task and switching the data areas in each switching of tasks.

**CONSTITUTION:** Task data areas 141 to 14N are fixedly allocated to a part of the address space of a microprocessor (MCPU) 11 and a task switching mechanism 15 is connected to the MCPU 11. The areas 141 to 14N store task data corresponding to respective tasks to be executed by the MCPU 11 and are respectively independently connected to the MCPU 11. The mechanism 15 switches respective areas 141 to 14N in accordance with the address specification of the MCPU 11. In the case of constructing a multitask system in the MCPU 11, the reliability of the system can be improved by separating respective tasks.



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